Exam 2

• Exam 2 – Friday November 20
  – See topics on next page
  – Sample problems online
  – Review - Monday Nov 16 and Wednesday Nov 18
  – Open book, open notes
  – Will be similar to homework since last exam
    • No assembly code
Exam 2

• Topics covered
  – RS232
  – Memory mapping
  – Interrupts
  – Timer system (input capture and output compare)
  – RTI system
  – A/D conversion

• If you don’t have a hard copy of the book, you may find it helpful to bring a printed description of the following registers:
  – RTICTL
  – CRGFLG, CRGINT
  – TSCR1, TSCR2
  – TCTL1, TCTL2, TCTL3, TCTL4
  – TIOS
  – TIE
  – TFLG1, TFLG2
  – SCIBD, SCICR1, SCICR2, SCISR1

• Open book, open notes

• It may be helpful to have a calculator